



Attorney Docket No. 5649-877

2822
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re: Cho et al.
Serial No.: 09/923,670
Filed: August 7, 2001
For: METHODS OF FABRICATING INTEGRATED CIRCUIT DEVICES HAVING
DIELECTRIC REGIONS PROTECTED WITH MULTI-LAYER INSULATION
STRUCTURES

Date: December 1, 2003

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

Applicants provide the present Amendment to address the issues raised in the Office Action mailed September 16, 2003. Applicants provide the present Amendment pursuant to the rules stated in revised 37 C.F.R. 1.121 that became effective on July 30, 2003.

It is not believed that an extension of time and/or additional fee(s), including fees for additional claims, are required, beyond those that may otherwise be provided for in documents accompanying this paper. In the event, however, that an extension of time is necessary to allow consideration of this paper, such an extension is hereby petitioned under 37 C.F.R. §1.136(a). Any additional fees believed to be due in connection with this paper may be charged to our Deposit Account No. 50-0220.

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